

REMARKS

New claims 50-56 have been added. Accordingly, claims 16-56 are pending in the application.

Claims 50-55 are identical to claims 1-6 of U.S. Patent No. 6,528,983 (the '983 patent), which issued on March 4, 2003, with one exception. The third line of Claim 1 of the '983 patent appears to contain an error in printing. The word "having" is printed as "halving". A study of the prosecution history of the '983 patent indicates that patentee correctly spelled this word as "having". Therefore, claim 50 of the present application recites "having" instead of "halving". Claim 56 is similar to claim 52 other than the recitation of a "reference potential point" instead of a "signal ground point". Applicants submit that claim 56 is substantially the same as claim 52.

It is submitted that claims 50-56 of the present application and claims 1-6 of the '983 patent correspond to a proposed count (hereinafter referred to as "the proposed count") presented in a Request for Interference filed on October 23, 2002 in the present application. A Supplemental Claim Support Chart is attached and shows the terms of each of

claims 50-56 applied to the disclosure of the present application.

The Request for interference was filed to provoke an interference between the present application and U.S. Patent No. 6,307,364 (the '364 patent) and U.S. Patent No. 6,329,809 (the '809 patent). The '983 patent issued from a divisional application of U.S. Patent Application Serial No. 09/384,679, which issued as the '364 patent.

The claims that issued in the '983 patent differ from the claims originally filed in the divisional application. During the prosecution of the '983 patent, an amendment was filed on July 9, 2002 that removed all reference to a "third RF signal input port". This amendment was filed in response to rejections made by the Examiner under 35 USC §112, first and second paragraphs, in an Office Action mailed on May 30, 2002. After this amendment, in an Office Action mailed on September 9, 2002, the Examiner rejected all of the pending claims under the judicially created doctrine of obviousness type double patenting as being unpatentable over claims 1-11 of the '364 patent. The Examiner stated in the Office Action that "although the conflicting claims are not identical, they are not patentably distinct from each other" (See Office Action mailed September 9, 2002, page 2, line 17). On October 10,

2002, a terminal disclaimer was filed, without objection, in order to obtain issuance of the '983 patent.

Therefore, it is once again submitted that claims 50-56 of the present application correspond to the proposed count. It is further submitted that claims 1-6 of the '983 patent correspond to the proposed count even though the '983 patent issued from a divisional application, in view of the amendments to the claims and the filing of the terminal disclaimer. As such, it is requested that this amendment be entered and that an Interference be declared between the present application and the '364 patent, the '809 patent and the '983 patent, which are commonly assigned. Pursuant to 35 U.S.C. §135(b), new claims 50-55 of the present application, which copy claims 1-6 of the '983 patent, are being presented in this amendment, which is being filed not more than one year from the issuance of the '983 patent.

Applicants also wish to advise the Examiner that a continuation application of this application was filed on October 23, 2002 and is currently pending as U.S. Patent Application Serial No. 10/277,633. This continuation application contains claims that were subjected to the restriction requirement and withdrawn from consideration in

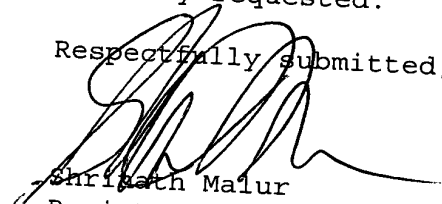
Serial No. 09/555,010

ASA-883

U.S. Patent Application Serial No. 09/384,679, which issued as  
the '364 patent,.

Entry of this amendment is hereby requested.

Respectfully submitted,



Shrinath Malur  
Registration No. 34,663  
Attorney for Applicants

MATTINGLY, STANGER & MALUR  
1800 Diagonal Rd., Suite 370  
Alexandria, Virginia 22314  
(703) 684-1120  
Date: March 3, 2004

# SUPPLEMENTAL CLAIM SUPPORT CHART

PENDING CLAIMS	CORRESPONDING SUPPORT IN U.S. SERIAL NO.09/555,010
<p>50. A system for sensing RF amplifier output power comprising:</p> <hr/> <p>a RF amplifier having an amplifier transistor and further having a first RF signal input port and a first RF signal output port;</p> <hr/> <p>a sampling amplifier having a sampling transistor, a second RF signal input port coupled to the first RF signal input port and further having a second RF signal output port, wherein the sampling transistor is physically smaller than the amplifier transistor; and</p> <hr/> <p>a current sensing network being operatively connected to said second RF signal output port, wherein said current sensing network has a bias input port.</p>	<p>(Fig. 1; pg 1, lines 2-8; and pg. 2, lines 9-14)</p> <hr/> <p>T1, Gate of T1, Drain of T1 (DRAIN 1) (Figs. 11 and 12; pg. 6, lines 10-12, pg. 20, lines 6-21)</p> <hr/> <p>T2, Gate of T2, Drain of T2 (DRAIN 2) (Figs. 11 and 12; pg. 2, line 23 - pg. 3, line 5; pg. 6, lines 16-19; pg.10, lines 11-14; pg. 20, lines 6-21; pg. 22, line 28 to pg. 23 line 12; pg. 25, lines 8-14; and original claim 1)</p> <hr/> <p>A circuit comprising Rs1, C2, and Vref terminal as a bias input port (Fig. 11; pg. 20, lines 15-21)</p>
<p>51. The system according to claim 50, wherein the current sensing network comprises a resistor coupled at one end to the second RF signal output port and coupled at an opposite end to the bias input port</p>	<p>Rs1 (Fig. 11; pg. 20, lines 15-21)</p>
<p>52. The system according to claim 51, wherein the current sensing network further comprises a capacitor coupled at one end to the second RF signal output port and coupled at an opposite end to a signal ground point associated with the system.</p>	<p>C2 (Fig. 11; pg. 20, lines 15-21)</p> <p>C2 can be seen as being coupled to an AC ground because C2 is coupled to Vref that is a DC voltage.</p>
<p>53. The system according to claim 52 further comprising a bias network.</p>	<p>Gain Control Circuit (4) (Figs. 1 and 2; pg. 6, lines 4-7; pg. 9, line 25 - pg. 10, line 5)</p>
<p>54. The system according to claim 53 further comprising a first bias resistor coupled at one end to the amplifier transistor and coupled at an opposite end to the bias network.</p>	<p>R1 (Figs. 2 and 11; pg. 9, line 25 - pg. 10, line 5; pg. 20, lines 10-13)</p>

Claim Support Chart  
Page 2

<p>55. The system according to claim 54 further comprising a second bias resistor coupled at one end to the sampling transistor and coupled at an opposite end to the bias network.</p>	<p>R1 in Fig. 11 corresponding to R2 in Fig. 2 (Figs. 2 and 11; pg. 9, line 25 - pg. 10, line 5; pg. 20, lines 10-13)</p> <p>R1 in Fig. 11 can be seen substantially the same as a set of R1 and R2 in Fig. 2, i.e. first and second bias resistor.</p>
<p>56. The system according to claim 51, wherein the current sensing network further comprises a capacitor coupled at one end to the second RF signal output port and coupled at an opposite end to a reference potential point associated with the system.</p>	<p>C2 (Fig. 11; pg. 20, lines 15-21)</p> <p>C2 is coupled to Vref, i.e. a reference potential point.</p> <p>Claim 56 is submitted to be substantially the same as Claim 52.</p>